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FPGA-Software Engineer

FPGA/Software Engineer

Description: Develop Verilog code and supporting software for imaging products. Our FPGA/Software implementations integrate with ADCs, digital signal processors (DSP), embedded processors, and hardware peripherals including USB3, Gigabit Ethernet, and Camera Link. We work with Xilinx FPGAs and use C# for interfacing.

- This role requires the ability to effectively collaborate with a cross functional design team. Candidates must possess solid interpersonal as well as excellent oral and written communication skills..

Responsibilities:

- Creating synthesizable VHDL/Verilog HDL code for FPGAs
- Creating code to simulate and verify FPGA designs
- Able to create interfaces to external hardware
- Ability to troubleshoot FPGA designs
- Able to analyze FPGA resource utilization, power, and timing
- Creating embedded software with C#
- Modify existing PC application code using C#

Basic Qualifications:

- Bachelors or Masters degree Computer Engineering, Electrical Engineering or a related discipline from an accredited college
- Access to export controlled information so must be a US Citizen.

Desired Skills:

- Experience with Verilog/VHDL programming
- Experience with C, C++, or C# programming
- FPGA simulation and synthesis
- Image processing experience

The Locale: Camarillo CA is a great place to live – close to Ventura and Oxnard beaches as well, just south of Carpinteria and Santa Barbara (30 minutes) and north of Los Angeles International Airport (1 hour).